	Type	Ref #	Hits	Search Text
1	BRS	S1	261	717/129.ccls.
2	BRS	S2	94	717/134.ccls.
3	BRS	S3	279	debug$5 and (S1 or S2)
4	BRS	S4	827	717/124.ccls.
5	BRS	S5	41	S4 and (address$3 near3 pointer$3)
6	BRS	S6	30	S5 and (break or monitor or comparat$5)
7	BRS	S7	35	S5 and (break or monitor or comparat$5 or interrupt$2)
8	BRS	S8	14	S4 and (compar$6 adj address$5)
9	BRS	S9	0	"6557119.pm"
10	BRS	S10	0	"6557119.pn"
11	IS&R	S11	1	("6557119").PN.
12	BRS	S12	1	S11 and emula$5
13	BRS	S13	927	debug with emulat$5
14	BRS	S14	261	717/129.ccls.
15	BRS	S15	94	717/134.ccls.
16	BRS	S16	827	717/124.ccls.
17	BRS	S17	59	S13 and (S14 or S15 or S16)
18	BRS	S18	21	S17 and comparat$5
19	IS&R	S19	0	("L3and(ROMor(readadjonly)orread)").PN.
20	IS&R	S20	0	("L3and(ROMor(readadjonly)orread)").PN.
21	IS&R	S21	0	("L3and(ROMor(readadjonly)orread)").PN.

	DBs	Time Stamp	Comments	Error Definition
1	US-PGPUB; USPAT; EPO	2006/10/03 15:48		
2	US-PGPUB; USPAT; EPO	2006/10/03 15:48		
3	US-PGPUB; USPAT; EPO	2006/10/03 15:50		
4	US-PGPUB; USPAT; EPO	2006/10/03 15:50		
5	US-PGPUB; USPAT; EPO	2006/10/04 09:55		
6	US-PGPUB; USPAT; EPO	2006/10/03 15:52		
7	US-PGPUB; USPAT; EPO	2006/10/03 16:20		
8	US-PGPUB; USPAT; EPO	2006/10/03 16:21		
9	US-PGPUB; USPAT; EPO	2006/10/04 09:55		
10	US-PGPUB; USPAT; EPO	2006/10/04 09:56		
11	US-PGPUB; USPAT; EPO	2006/10/04 11:10		
12	US-PGPUB; USPAT; EPO	2006/10/04 10:12		
13	US-PGPUB; USPAT; EPO	2006/10/04 10:13		
14	US-PGPUB; USPAT; EPO	2006/10/04 10:13		
15	US-PGPUB; USPAT; EPO	2006/10/04 10:13		
16	US-PGPUB; USPAT; EPO	2006/10/04 10:13		
17	US-PGPUB; USPAT; EPO	2006/10/04 10:13		
18	US-PGPUB; USPAT; EPO	2006/10/04 10:13		
19	US-PGPUB; USPAT; EPO	2006/10/04 11:11		
20	US-PGPUB; USPAT; EPO	2006/10/04 11:11		
21	US-PGPUB; USPAT; EPO	2006/10/04 11:11		

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	Type	Ref #	Hits	Search Text
22	BRS	S22	1	S11 and (ROM or (read adj only) or read)
23	BRS	S23	0	S11 and microprocessor and debug and monitor and break and address and controller
24	BRS	S24	0	S11 and debug and monitor and break and address and controller
25	BRS	S25	1	S11 and monitor
26	BRS	S26	0	S11 and break
27	BRS	S27	0	S11 and (break$4 adj point$3)
28	BRS	S28	1	S11 and (address)
29	IS&R	S29	1	("7043717").PN.
30	BRS	S30	0	S29 and microcomputer$2
31	BRS	S31	1	S29 and port
32	BRS	S32	0	S29 and port and signal$3
33	BRS	S33	1	S29 and port and break
34	BRS	S34	0	"6173419.pn"
35	IS&R	S35	1	("6173419").PN.
36	BRS	S36	1	S35 and (break$2 adj point$2)
37	BRS	S37	0	S35 and (break$2 adj point$2)and mrico$9
38	BRS	S38	0	S35 and (break$2 adj point$2)and microcomputer
39	BRS	S39	0	S35 and (break$2 adj point$2)and microprocee$3
40	BRS	S40	1	S35 and (break$2 adj point$2)and microproces$3
41	BRS	S41	1	S35 and (break$2 adj point$2)and microproces$3 and host
42	BRS	S42	1	S35 and (break$2 adj point$2)and microproces$3 and host and signal

	DBs	Time Stamp	Comments	Error Definition
22	US-PGPUB; USPAT; EPO	2006/10/04 11:13		
23	US-PGPUB; USPAT; EPO	2006/10/04 11:14		
24	US-PGPUB; USPAT; EPO	2006/10/04 11:14		
25	US-PGPUB; USPAT; EPO	2006/10/04 11:15		
26	US-PGPUB; USPAT; EPO	2006/10/04 11:15		
27	US-PGPUB; USPAT; EPO	2006/10/04 11:15		
28	US-PGPUB; USPAT; EPO	2006/10/04 11:17		
29	US-PGPUB; USPAT; EPO	2006/10/04 11:17		
30	US-PGPUB; USPAT; EPO	2006/10/04 11:17		
31	US-PGPUB; USPAT; EPO	2006/10/04 11:17		
32	US-PGPUB; USPAT; EPO	2006/10/04 11:17		
33	US-PGPUB; USPAT; EPO	2006/10/04 11:18		
34	US-PGPUB; USPAT; EPO	2006/10/04 11:19		
35	US-PGPUB; USPAT; EPO	2006/10/04 11:19		
36	US-PGPUB; USPAT; EPO	2006/10/04 11:20		
37	US-PGPUB; USPAT; EPO	2006/10/04 11:20		
38	US-PGPUB; USPAT; EPO	2006/10/04 11:20		
39	US-PGPUB; USPAT; EPO	2006/10/04 11:20		
40	US-PGPUB; USPAT; EPO	2006/10/04 11:21		
41	US-PGPUB; USPAT; EPO	2006/10/04 11:21		
42	US-PGPUB; USPAT; EPO	2006/10/04 11:21		

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	Type	Ref #	Hits	Search Text
43	BRS	S43	1	S35 and (break$2 adj point$2)and microproces$3 and host and signal$3
44	BRS	S44	0	S35 and (break$2 adj point$2)and microproces$3 and host and signal$3 and pointer$2
45	BRS	S45	0	S35 and (break$2 adj point$2)and microproces$3 and host and signal$3 and pointer
46	BRS	S46	1	S35 and (break$2 adj point$2)and microproces$3 and host and signal$3 and point$2
47	BRS	S47	0	S35 and (break$2 adj point$2)and microproces$3 and host and signal$3 and interrupt
48	BRS	S48	0	S35 and (break$2 adj point$2)and microproces$3 and host and signal$3 and interrupt$2
49	BRS	S49	0	S35 and (break$2 adj point$2)and microproces$3 and host and signal$3 and interrupt$5
50	BRS	S50	1	S35 and (break$2 adj point$2)and microproces$3 and host and signal$3 and monitor
51	BRS	S51	1	S35 and (break$2 adj point$2)and microproces$3 and host and signal$3 and monitor and comparator
52	IS&R	S52	1	("5943498").PN.
53	BRS	S53	1	S52 and emulator
54	BRS	S54	1	S52 and emulator and host
55	BRS	S55	1	S52 and emulator and host and break
56	BRS	S56	1	S52 and emulator and host and break and rom
57	BRS	S57	1	S52 and emulator and host and break and rom and signal
58	BRS	S58	1	S52 and emulator and host and break and rom and signal and monitor
59	BRS	S59	1	S52 and emulator and host and break and rom and signal and monitor and program

	DBs	Time Stamp	Comments	Error Definition
43	US-PGPUB; USPAT; EPO	2006/10/04 11:21		
44	US-PGPUB; USPAT; EPO	2006/10/04 11:22		
45	US-PGPUB; USPAT; EPO	2006/10/04 11:22		
46	US-PGPUB; USPAT; EPO	2006/10/04 11:22		
47	US-PGPUB; USPAT; EPO	2006/10/04 11:23		
48	US-PGPUB; USPAT; EPO	2006/10/04 11:23		
49	US-PGPUB; USPAT; EPO	2006/10/04 11:23		
50	US-PGPUB; USPAT; EPO	2006/10/04 11:23		
51	US-PGPUB; USPAT; EPO	2006/10/04 12:28		
52	US-PGPUB; USPAT; EPO	2006/10/04 12:28		
53	US-PGPUB; USPAT; EPO	2006/10/04 12:28		
54	US-PGPUB; USPAT; EPO	2006/10/04 12:28		
55	US-PGPUB; USPAT; EPO	2006/10/04 12:28		
56	US-PGPUB; USPAT; EPO	2006/10/04 12:29		
57	US-PGPUB; USPAT; EPO	2006/10/04 12:29		
58	US-PGPUB; USPAT; EPO	2006/10/04 12:29		
59	US-PGPUB; USPAT; EPO	2006/10/04 12:29		

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	Type	Ref #	Hits	Search Text
60	BRS	S60	1	S52 and emulator and host and break and rom and signal and monitor and program and interrupt
61	BRS	S61	0	S52 and emulator and host and break and rom and signal and monitor and program and interrupt and temporary
62	BRS	S62	1	S52 and emulator and host and break and rom and signal and monitor and program and interrupt
63	IS&R	S63	1	("2002\0059560").PN.
64	BRS	S64	1	S63 and microprocessor
65	BRS	S65	0	S63 and microprocessor and break and controller
66	BRS	S66	1	S63 and microprocessor and break and control$3
67	BRS	S67	1	S63 and microprocessor and break and control$3 and signal
68	BRS	S68	1	S63 and microprocessor and break and control$3 and signal and address
69	BRS	S69	1	S63 and microprocessor and break and control$3 and signal and address and holder and rom
70	BRS	S70	1	S63 and microprocessor and break and control$3 and signal and address and holder and rom and host
71	BRS	S71	0	S63 and microprocessor and break and control$3 and signal and address and holder and rom and host and interrupt
72	BRS	S72	0	S63 and microprocessor and break and control$3 and signal and address and holder and rom and host and comparator
73	BRS	S73	1	S63 and microprocessor and break and control$3 and signal and address and holder and rom and host
74	BRS	S74	1	S52 and emulator and host and break and rom and signal and monitor and program and interrupt and compare$2
75	BRS	S75	33	"5943498"

	DBs	Time Stamp	Comments	Error Definition
60	US-PGPUB; USPAT; EPO	2006/10/04 12:29		
61	US-PGPUB; USPAT; EPO	2006/10/04 12:29		
62	US-PGPUB; USPAT; EPO	2006/10/04 12:36		
63	US-PGPUB; USPAT; EPO	2006/10/04 12:34		
64	US-PGPUB; USPAT; EPO	2006/10/04 12:35		
65	US-PGPUB; USPAT; EPO	2006/10/04 12:35		
66	US-PGPUB; USPAT; EPO	2006/10/04 12:35		
67	US-PGPUB; USPAT; EPO	2006/10/04 12:35		
68	US-PGPUB; USPAT; EPO	2006/10/04 12:35		
69	US-PGPUB; USPAT; EPO	2006/10/04 12:35		
70	US-PGPUB; USPAT; EPO	2006/10/04 12:36		
71	US-PGPUB; USPAT; EPO	2006/10/04 12:36		
72	US-PGPUB; USPAT; EPO	2006/10/04 12:36		
73	US-PGPUB; USPAT; EPO	2006/10/04 12:36		
74	US-PGPUB; USPAT; EPO	2006/10/04 14:37		
75	US-PGPUB; USPAT; EPO	2006/10/04 14:37		

	Errors
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	Type	Ref #	Hits	Search Text
76	IS&R	S76	1	("5943498").PN.
77	BRS	S77	32	("5943498").URPN.
78	BRS	S78	1	S76 AND (HOST ADJ COMPUTER)
79	IS&R	S79	1	("6557119").PN.
80	BRS	S80	1	S79 and monitor
81	IS&R	S81	1	("6144992").PN.
82	IS&R	S82	0	("(S1orS2orS4)and(remoteadjdebug$5)").PN.
83	BRS	S83	263	717/129.ccls.
84	BRS	S84	94	717/134.ccls.
85	BRS	S85	830	717/124.ccls.
86	BRS	S86	35	(S83 or S84 or S85)and(remote adj debug$5)
87	BRS	S87	14	S86 and microprocessor
88	BRS	S88	585	MICROPROCESSOR with debug
89	BRS	S89	146	S88 and remote
90	BRS	S90	115	S89 and compar$5
91	BRS	S91	112	S90 and address$3
92	BRS	S92	63	S91 and break
93	BRS	S93	86	S91 and (break or pause or stop)
94	BRS	S94	94	S91 and (break or pause or stop or interrupt)
95	BRS	S95	94	S94 and control$4
96	BRS	S96	71	S95 and host

	DBs	Time Stamp	Comments	Error Definition
76	US-PGPUB; USPAT; EPO	2006/10/04 15:02		
77	USPAT	2006/10/04 14:39		
78	US-PGPUB; USPAT; EPO	2006/10/04 15:55		
79	US-PGPUB; USPAT; EPO	2006/10/04 15:55		
80	US-PGPUB; USPAT; EPO	2006/10/05 07:46		
81	US-PGPUB; USPAT; EPO	2006/10/05 10:06		
82	US-PGPUB; USPAT; EPO	2006/10/05 10:07		
83	US-PGPUB; USPAT; EPO	2006/10/05 10:07		
84	US-PGPUB; USPAT; EPO	2006/10/05 10:07		
85	US-PGPUB; USPAT; EPO	2006/10/05 10:07		
86	US-PGPUB; USPAT; EPO	2006/10/05 10:08		
87	US-PGPUB; USPAT; EPO	2006/10/05 10:19		
88	US-PGPUB; USPAT; EPO	2006/10/05 10:19		
89	US-PGPUB; USPAT; EPO	2006/10/05 10:19		
90	US-PGPUB; USPAT; EPO	2006/10/05 10:19		
91	US-PGPUB; USPAT; EPO	2006/10/05 10:19		
92	US-PGPUB; USPAT; EPO	2006/10/05 10:19		
93	US-PGPUB; USPAT; EPO	2006/10/05 10:20		
94	US-PGPUB; USPAT; EPO	2006/10/05 10:20		
95	US-PGPUB; USPAT; EPO	2006/10/05 10:20		
96	US-PGPUB; USPAT; EPO	2006/10/05 10:20		

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	Type	Ref #	Hits	Search Text
97	BRS	S97	93	S95 and (host or computer or port or transmit)
98	IS&R	S98	1	("5379301").PN.
99	BRS	S99	0	S98 and host
100	BRS	S100	0	S98 and remote'
101	BRS	S101	0	S98 and remote
102	BRS	S102	0	S98 and device
103	BRS	S103	4747	program adj monitor$5
104	BRS	S104	373	S103 and debug
105	BRS	S105	284	S104 and (target or host)
106	BRS	S106	276	S105 and computer
107	BRS	S107	277	S105 and (address or communicate)
108	BRS	S108	0	S107 and "717".ccls.
109	BRS	S109	72	S107 and "717".clas.
110	BRS	S110	7843	break adj point
111	BRS	S111	336	S110 with compar$5
112	BRS	S112	263	717/129.ccls.
113	BRS	S113	94	717/134.ccls.
114	BRS	S114	830	717/124.ccls.
115	BRS	S115	7	S111 and (S112 or S113 or S114)
116	IS&R	S116	1	("6145123").PN.
117	BRS	S117	2	2002\0064646

	DBs	Time Stamp	Comments	Error Definition
97	US-PGPUB; USPAT; EPO	2006/10/05 11:33		
98	US-PGPUB; USPAT; EPO	2006/10/05 11:33		
99	US-PGPUB; USPAT; EPO	2006/10/05 11:33		
100	US-PGPUB; USPAT; EPO	2006/10/05 11:33		
101	US-PGPUB; USPAT; EPO	2006/10/05 11:33		
102	US-PGPUB; USPAT; EPO	2006/10/05 15:22		
103	US-PGPUB; USPAT; EPO	2006/10/05 15:23		
104	US-PGPUB; USPAT; EPO	2006/10/05 15:23		
105	US-PGPUB; USPAT; EPO	2006/10/05 15:23		
106	US-PGPUB; USPAT; EPO	2006/10/05 15:23		
107	US-PGPUB; USPAT; EPO	2006/10/05 15:23		
108	US-PGPUB; USPAT; EPO	2006/10/05 15:24		
109	US-PGPUB; USPAT; EPO	2006/10/05 16:30		
110	US-PGPUB; USPAT; EPO	2006/10/05 16:30		
111	US-PGPUB; USPAT; EPO	2006/10/05 16:30		
112	US-PGPUB; USPAT; EPO	2006/10/05 16:30		
113	US-PGPUB; USPAT; EPO	2006/10/05 16:30		
114	US-PGPUB; USPAT; EPO	2006/10/05 16:30		
115	US-PGPUB; USPAT; EPO	2006/10/05 16:37		
116	US-PGPUB; USPAT; EPO	2006/10/05 16:37		
117	US-PGPUB; USPAT; EPO	2006/10/05 16:38		

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	Type	Ref #	Hits	Search Text
118	BRS	S118	2	2002/0064646
119	BRS	S119	2	2002\0064646
120	BRS	S120	1	2002\0188813
121	BRS	S121	154	two adj break adj point
122	BRS	S122	99	S121 and compar$5
123	BRS	S123	2	S122 and "717".clas.
124	BRS	S124	21	S122 and debug
125	BRS	S125	4	S121 and "717".clas.
126	BRS	S126	7853	break adj point
127	BRS	S127	251	S126 and "717".clas.
128	BRS	S128	148	S127 and compar$5
129	BRS	S129	13	S128 and (control adj signal)
130	BRS	S130	1	10/605196
131	IS&R	S131	1	("6145123").PN.
132	BRS	S132	3440	emulat$5 and debug
133	BRS	S133	411	S132 and "717".clas.
134	BRS	S134	244	S133 AND rom
135	BRS	S135	57	S134 AND COMPARA$5
136	BRS	S136	34	"4463421"
137	IS&R	S137	1	("4463421").PN.
138	IS&R	S138	1	("4630195").PN.

	DBs	Time Stamp	Comments	Error Definition
118	US-PGPUB; USPAT; EPO	2006/10/05 16:38		
119	US-PGPUB; USPAT; EPO	2006/10/05 16:41		
120	US-PGPUB; USPAT; EPO	2006/10/05 16:41		
121	US-PGPUB; USPAT; EPO	2006/10/11 16:12		
122	US-PGPUB; USPAT; EPO	2006/10/11 16:08		
123	US-PGPUB; USPAT; EPO	2006/10/11 16:09		
124	US-PGPUB; USPAT; EPO	2006/10/11 16:09		
125	US-PGPUB; USPAT; EPO	2006/10/11 16:11		
126	US-PGPUB; USPAT; EPO	2006/10/11 16:12		
127	US-PGPUB; USPAT; EPO	2006/10/11 16:12		
128	US-PGPUB; USPAT; EPO	2006/10/11 16:12		
129	US-PGPUB; USPAT; EPO	2006/10/11 16:12		
130	US-PGPUB; USPAT; EPO	2006/10/12 11:07		
131	US-PGPUB; USPAT; EPO	2006/10/12 13:32		
132	US-PGPUB; USPAT; EPO	2006/10/12 13:32		
133	US-PGPUB; USPAT; EPO	2006/10/12 13:32		
134	US-PGPUB; USPAT; EPO	2006/10/12 13:33		
135	US-PGPUB; USPAT; EPO	2006/10/12 16:26		
136	US-PGPUB; USPAT; EPO	2006/10/12 16:26		
137	US-PGPUB; USPAT; EPO	2006/10/12 16:26		
138	US-PGPUB; USPAT; EPO	2006/10/12 16:27		

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	Type	Ref #	Hits	Search Text
139	IS&R	S139	1	("4644469").PN.
140	BRS	S140	585	debug with microprocessor
141	BRS	S141	252	S140 and host
142	BRS	S142	252	S140 and host
143	BRS	S143	144	S142 and (control adj signal)
144	BRS	S144	588	debug near7 microprocessor
145	BRS	S145	167	S144 and host
146	BRS	S146	155	S145 and control
147	BRS	S147	122	S146 and monitor
148	IS&R	S148	1	("6934886").PN.
149	BRS	S149	7241	debugging and (microprocessor or MPU)
150	BRS	S150	6337	S149 and (monitor or sense or look or seek or status)
151	BRS	S151	6311	S150 and (host or server or computer or system)
152	BRS	S152	4323	S151 and controller
153	BRS	S153	3419	S152 and (compare or comparator or match or equal)
154	BRS	S154	301	S153 and "717".clas.
155	BRS	S155	21	S154 and debug.ti.
156	BRS	S156	364	debug.ti.
157	BRS	S157	16	S156 and microprocessor.ti.

	DBs	Time Stamp	Comments	Error Definition
139	US-PGPUB; USPAT; EPO	2006/10/12 16:27		
140	US-PGPUB; USPAT; EPO	2006/10/12 16:40		
141	US-PGPUB; USPAT; EPO	2006/10/12 16:40		
142	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/10/12 16:40		
143	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/10/12 16:41		
144	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/10/12 16:41		
145	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/10/12 16:42		
146	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/10/12 16:42		
147	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/10/13 08:16		
148	US-PGPUB; USPAT; EPO	2006/10/13 08:19		
149	US-PGPUB; USPAT; EPO	2006/10/13 08:19		
150	US-PGPUB; USPAT; EPO	2006/10/13 08:20		
151	US-PGPUB; USPAT; EPO	2006/10/13 08:20		
152	US-PGPUB; USPAT; EPO	2006/10/13 08:21		
153	US-PGPUB; USPAT; EPO	2006/10/13 08:21		
154	US-PGPUB; USPAT; EPO	2006/10/13 08:21		
155	US-PGPUB; USPAT; EPO	2006/10/13 08:22		
156	US-PGPUB; USPAT; EPO	2006/10/13 08:23		
157	US-PGPUB; USPAT; EPO	2006/10/13 08:23		

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	Type	Ref #	Hits	Search Text
158	BRS	S158	41	("5978937").URPN.
159	BRS	S159	465352	ROM monitor
160	BRS	S160	0	"ROM monitor".ti.
161	BRS	S161	316	ROM and monitor with debug
162	BRS	S162	182	ROM with debug
163	BRS	S163	129	S162 and monitor
164	BRS	S164	24	S158 and ROM
165	BRS	S165	19	S158 and host
166	BRS	S166	0	"microprocessor.ti"
167	BRS	S167	2516	microprocessor.ti.
168	BRS	S168	601	S167 and system.ti.
169	BRS	S169	3	S168 and debug.ti.
170	BRS	S170	19	microprocessor with (software adj debug)
171	BRS	S171	119	debug same (user adj program)
172	BRS	S172	71	S171 and microprocessor
173	IS&R	S173	1	("5978937").PN.
174	BRS	S174	1	S173 and computer
175	BRS	S175	1	S173 and debugger
176	BRS	S176	701	debug adj system
177	BRS	S177	599009	S176 and ROM monitor
178	BRS	S178	6	S176 and (ROM adj monitor)

	DBs	Time Stamp	Comments	Error Definition
158	USPAT	2006/10/13 09:55		
159	USPAT	2006/10/13 09:10		
160	USPAT	2006/10/13 09:11		
161	USPAT	2006/10/13 09:12		
162	USPAT	2006/10/13 09:12		
163	USPAT	2006/10/13 09:12		
164	USPAT	2006/10/13 09:15		
165	USPAT	2006/10/13 09:26		
166	USPAT	2006/10/13 09:26		
167	USPAT	2006/10/13 09:26		
168	USPAT	2006/10/13 09:26		
169	USPAT	2006/10/13 09:34		
170	USPAT	2006/10/13 09:41		
171	USPAT	2006/10/13 09:41		
172	USPAT	2006/10/13 09:41		
173	US-PGPUB; USPAT; EPO	2006/10/13 09:55		
174	US-PGPUB; USPAT; EPO	2006/10/13 09:56		
175	US-PGPUB; USPAT; EPO	2006/10/13 10:12		
176	US-PGPUB; USPAT; EPO	2006/10/13 10:12		
177	US-PGPUB; USPAT; EPO	2006/10/13 10:12		
178	US-PGPUB; USPAT; EPO	2006/10/13 10:32		

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	Type	Ref #	Hits	Search Text
179	BRS	S179	0	debug adj "system.ti"
180	BRS	S180	0	"debug adj system".ti.
181	BRS	S181	1	10/605196
182	BRS	S182	1	S181 and monitor
183	IS&R	S183	1	("6934886").PN.
184	BRS	S184	1	S183 and monitor
185	BRS	S185	576	717/127.ccls.
186	BRS	S186	157	S185 and debug
187	IS&R	S187	1	("6934886").PN.
188	BRS	S188	1	S187 and memory
189	IS&R	S189	1	("6934886").PN.
190	BRS	S190	1	S189 and control$4
191	BRS	S191	0	S189 and connect
192	BRS	S192	0	S189 and wire
193	BRS	S193	0	S189 and attach
194	BRS	S194	0	S189 and connect
195	BRS	S195	0	S189 and connect$5
196	BRS	S196	1	S189 and signal$3
197	BRS	S197	0	S189 and pointer
198	BRS	S198	1	S189 and point
199	BRS	S199	0	S189 and status

	DBs	Time Stamp	Comments	Error Definition
179	US-PGPUB; USPAT; EPO	2006/10/13 10:32		
180	US-PGPUB; USPAT; EPO	2006/10/13 10:32		
181	US-PGPUB; USPAT; EPO	2006/10/13 10:37		
182	US-PGPUB; USPAT; EPO	2006/10/13 10:37		
183	US-PGPUB; USPAT; EPO	2006/10/13 10:38		
184	US-PGPUB; USPAT; EPO	2006/10/13 10:50		
185	US-PGPUB; USPAT; EPO	2006/10/13 10:51		
186	US-PGPUB; USPAT; EPO	2006/10/13 13:31		
187	US-PGPUB; USPAT; EPO	2006/10/13 13:33		
188	US-PGPUB; USPAT; EPO	2006/10/13 13:33		
189	US-PGPUB; USPAT; EPO	2006/10/16 08:59		
190	US-PGPUB; USPAT; EPO	2006/10/16 09:14		
191	US-PGPUB; USPAT; EPO	2006/10/16 09:14		
192	US-PGPUB; USPAT; EPO	2006/10/16 09:14		
193	US-PGPUB; USPAT; EPO	2006/10/16 09:14		
194	US-PGPUB; USPAT; EPO	2006/10/16 09:15		
195	US-PGPUB; USPAT; EPO	2006/10/16 09:18		
196	US-PGPUB; USPAT; EPO	2006/10/16 09:50		
197	US-PGPUB; USPAT; EPO	2006/10/16 09:50		
198	US-PGPUB; USPAT; EPO	2006/10/16 10:16		
199	US-PGPUB; USPAT; EPO	2006/10/16 10:16		

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	Type	Ref #	Hits	Search Text
200	BRS	S200	1	S189 and (host adj computer)
201	BRS	S201	1	S189 and (data adj memory)
202	BRS	S202	1	S189 and (address adj register)
203	BRS	S203	1	S189 and (host adj computer)
204	BRS	S204	1	S189 and (program adj memory)
205	BRS	S205	2251	(read adj only) with (program adj memory)
206	BRS	S206	122	S205 and debug$5
207	BRS	S207	9	S206 and "717".clas.
208	BRS	S208	0	"debug.ti"
209	BRS	S209	364	debug.ti.
210	BRS	S210	100	S209 and (read adj only)
211	BRS	S211	2	S209 and ((read adj only) with (program adj memory))
212	BRS	S212	1	2002\0065646
213	BRS	S213	1	S212 aND rom
214	BRS	S214	1	S212 aND (READ ADJ ONLY)
215	BRS	S215	106031	1! AND ROM
216	BRS	S216	0	S189 AND ROM
217	BRS	S217	1	S189 AND READ
218	BRS	S218	0	S189 AND RMEMORY
219	BRS	S219	1	S189 AND MEMORY
220	BRS	S220	1	S212 AND (PROGRAM ADJ MEMORY)

	DBs	Time Stamp	Comments	Error Definition
200	US-PGPUB; USPAT; EPO	2006/10/16 10:31		
201	US-PGPUB; USPAT; EPO	2006/10/16 10:56		
202	US-PGPUB; USPAT; EPO	2006/10/16 11:01		
203	US-PGPUB; USPAT; EPO	2006/10/16 12:49		
204	US-PGPUB; USPAT; EPO	2006/10/16 12:59		
205	US-PGPUB; USPAT; EPO	2006/10/16 13:00		
206	US-PGPUB; USPAT; EPO	2006/10/16 13:00		
207	US-PGPUB; USPAT; EPO	2006/10/16 13:02		
208	US-PGPUB; USPAT; EPO	2006/10/16 13:02		
209	US-PGPUB; USPAT; EPO	2006/10/16 13:02		
210	US-PGPUB; USPAT; EPO	2006/10/16 13:02		
211	US-PGPUB; USPAT; EPO	2006/10/16 14:04		
212	US-PGPUB; USPAT; EPO	2006/10/16 14:04		
213	US-PGPUB; USPAT; EPO	2006/10/16 14:08		
214	US-PGPUB; USPAT; EPO	2006/10/16 14:10		
215	US-PGPUB; USPAT; EPO	2006/10/16 14:10		
216	US-PGPUB; USPAT; EPO	2006/10/16 14:10		
217	US-PGPUB; USPAT; EPO	2006/10/16 14:11		
218	US-PGPUB; USPAT; EPO	2006/10/16 14:11		
219	US-PGPUB; USPAT; EPO	2006/10/16 14:14		
220	US-PGPUB; USPAT; EPO	2006/10/16 14:52		

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	Type	Ref #	Hits	Search Text
221	BRS	S221	1	S189 and monitor
222	BRS	S222	1	S189 and (host adj computer)
223	BRS	S223	1	S189 and (debugger adj controller)
224	BRS	S224	0	S189 and "Fig. 3"
225	BRS	S225	0	S212 and compara$5
226	BRS	S226	1	S212 and breakpoint
227	BRS	S227	14	(two adj comparator$3) with debug$5
228	BRS	S228	3	(two adj comparator$3) with (break adj point)
229	BRS	S229	1	(two adj break adj point adj comparators)
230	BRS	S230	28497	(two near5 comparators)
231	BRS	S231	16	(two near5 comparators) with debug
232	BRS	S232	1	S189 and register
233	BRS	S233	1	S212 and breakpoint
234	BRS	S234	691	comparators with break
235	BRS	S235	62	S234 and debug

	DBs	Time Stamp	Comments	Error Definition
221	US-PGPUB; USPAT; EPO	2006/10/16 14:53		
222	US-PGPUB; USPAT; EPO	2006/10/16 14:54		
223	US-PGPUB; USPAT; EPO	2006/10/16 15:12		
224	US-PGPUB; USPAT; EPO	2006/10/16 15:14		
225	US-PGPUB; USPAT; EPO	2006/10/16 15:15		
226	US-PGPUB; USPAT; EPO	2006/10/16 15:48		
227	US-PGPUB; USPAT; EPO	2006/10/16 15:59		
228	US-PGPUB; USPAT; EPO	2006/10/16 16:07		
229	US-PGPUB; USPAT; EPO	2006/10/16 16:11		
230	US-PGPUB; USPAT; EPO	2006/10/16 16:11		
231	US-PGPUB; USPAT; EPO	2006/10/16 16:14		
232	US-PGPUB; USPAT; EPO	2006/10/16 16:21		
233	US-PGPUB; USPAT; EPO	2006/10/16 16:40		
234	US-PGPUB; USPAT; EPO	2006/10/16 16:47		
235	US-PGPUB; USPAT; EPO	2006/10/17 08:04		

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PLUS Search Results for S/N 10605196, Searched Tue Oct 03 09:02:55 EDT 2006
The Patent Linguistics Utility System (PLUS) is a USPTO automated search system
for U.S. Patents from 1971 to the present PLUS is a query-by-example search system which
produces a list of patents that are most closely related linguistically to the application
searched. This search was prepared by the staff of the Scientific and Technical Information
Center, SIRA.

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